

M.Tech (ES&VLSI)

ISO 9001:2015 AICTE-CII: GOLD Category Institute NAAC-'A' Grade Institute (CGPA: 3.21) NIRF-2020 Rank Band: 201-250

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE
 Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.
 కాకతీయ ప్రేచ్ఛాగికి एवं विज्ञान संस्थान, వరంగల్ - 506 015 తెలంగాణ, భారత
 ಕಾಕತಿಯ ಸಾಂಕೇತಿಕ ವಿಜ್ಞಾನ ಶಾಸ್ತ್ರ ವಿಧ್ಯಾಲಯಂ, ವರಂಗಲ್ - 506 015 తెలంగాణ, భారతదేశము

KITSW (An Autonomous Institute under Kakatiya University, Warangal)
 (Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
M.Tech. CLASSWORK TIMETABLE for the EVEN SEMESTER of Academic Year 2021-22

II SEMESTER of M. Tech (ES&VLSI)

Class Teacher : Sri O.Anjaneyulu

w.e.f 11.04.2022

Day	9.40am - 10.30am	10.30am - 11.20am	11.20am - 12.10pm	12.10pm - 1.00pm	LUNCH BREAK	2.00pm - 2.50pm	2.50pm - 3.40pm	3.40pm - 4.30pm
Monday	ASCD BI-219	<-----AICD Lab-----> (B I- 203-CHS)				ESDWRTOS BI-201	LVLSI BI-201	AICD BI-201
Tuesday	VE B V-102	<----- ASCD Lab-----> (B I-201-MQ)				ESDWRTOS BI-217	LIBRARY	
Wednesday	ESDWRTOS BI-217	<-----AICD Lab-----> (B I- 203-CHS)				LVLSI(T) BI-219	AICD BI-219	ASCD(T) BI-219
Thursday	VE BI-217	ASCD BI-217	LVLSI BI-217	LIBRARY		AICD(T) BI-217	ESDWRTOS (T) B I-218	LIBRARY
Friday	AICD BI-217	ASCD BI-217	LVLSI BI-217	VE(T) BI-217		<----- ASCD Lab-----> (B I-201-MQ)		
Saturday	-	-	-	-				

Course Title	Course Code & Course Name	Course Faculty
AICD	P20EV201: Analog IC Design	Dr. CH. Sridevi (CHS)
ASCD	P20EV202: Advanced System On Chip Design	Mr. MD. Abdul Muqueem (MQ)
LVLSI	P20EV203A: Low Power VLSI Design	Sri O.Anjaneyulu (OA)
ESDWRTOS	P20EV204A: Embedded Systems Design with RTOS	Smt. K. Shailaja(KSJ)
VE	P20AC208B: Value Education	Sri B.Krishna Sundeep (BKS)
AICD Lab	P20EV205: Analog IC Design Laboratory	Dr. CH. Sridevi(CHS)
ASCD Lab	P20EV206: Advanced System On Chip Design Laboratory	Mr. MD. Abdul Muqueem (MQ)

[Signature]
 Faculty I/c,
 Dept.Timetables

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 Faculty I/c,
 Overall
 Timetables

[Signature]
 Programme Head,
 ECI

[Signature]
 HoD, ECE

[Signature]
 Dean, AA

[Signature]
 Principal